

< High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

# CM600E1A-66X

HIGH POWER SWITCHING USE  
INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

## CM600E1A-66X



- $I_C$  ..... 600A
- $V_{CES}$  ..... 3300V
- 2-elements in a Pack (for brake chopper\*)
- Insulated Type (Al base type)
- CSTBT™(III) / RFC Diode

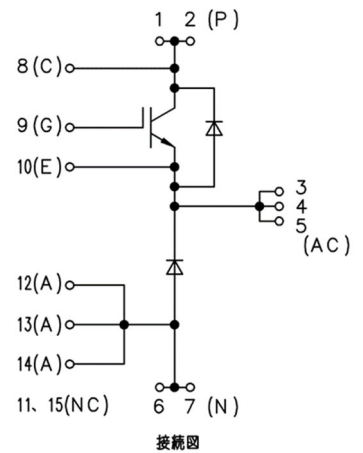
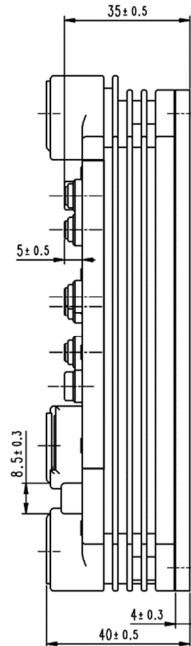
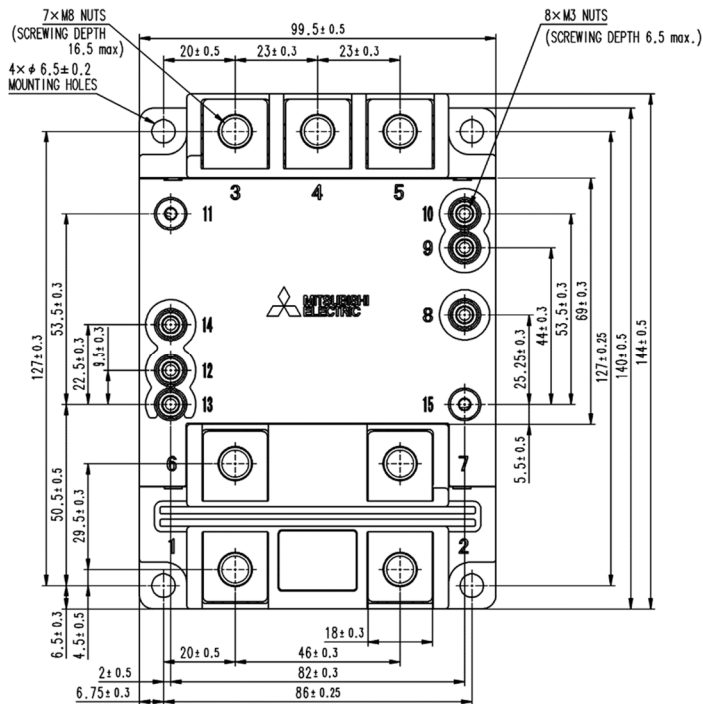
\*Not allowed for 3-level use. Limited to use as a brake chopper.

## APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers

### OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



No.12, 13, 14 MUST be open electrically.

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**MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
$V_{CES}$	Collector-emitter voltage	$V_{GE} = 0V, T_j = -40...+150^{\circ}C$	3300	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$	3200	
$V_{GES}$	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	$\pm 20$	V
$I_C$	Collector current	DC, $T_c = 109^{\circ}C$	600	A
$I_{CRM}$		Pulse (Note 1)	1200	A
$I_E$	Emitter current (Note 2)	DC, $T_c = 90^{\circ}C$	600	A
$I_{ERM}$		Pulse (Note 1)	1200	A
$I_F$	Forward current (Note 3)	DC, $T_c = 90^{\circ}C$	600	A
$I_{FRM}$		Pulse (Note 1)	1200	A
$P_{tot}$	Maximum power dissipation (Note 4)	$T_c = 25^{\circ}C$ , IGBT part	5400	W
$V_{iso}$	Isolation voltage	RMS, sinusoidal, $f = 60Hz, t = 1 \text{ min.}, T_c = 25^{\circ}C$	6000	V
$Q_{PD}$	Partial discharge	Charged part to the baseplate $V1 = 3500 \text{ Vrms}, V2 = 2600 \text{ Vrms}$ AC 60 Hz, $T_c = 25^{\circ}C$ (acc. to IEC 61287)	10	pC
$T_j$	Junction temperature		$-50 \sim +150$	$^{\circ}C$
$T_{jop}$	Operating junction temperature		$-50 \sim +150$	$^{\circ}C$
$T_{stg}$	Storage temperature		$-55 \sim +125$	$^{\circ}C$
$t_{psc}$	Short circuit pulse width	$V_{CC} = 2400V, V_{CE} \leq V_{CES}, V_{GE} = 15V, T_j = 150^{\circ}C$ $R_{g(on)} = 2.2\Omega, R_{g(off)} = 51\Omega, C_{GE} = 33nF$	10	$\mu s$

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## ELECTRICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit		
			Min	Typ	Max			
$I_{CES}$	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	$T_j = 25^\circ C$	—	—	2.0	mA	
			$T_j = 125^\circ C$	—	2.0	—		
			$T_j = 150^\circ C$	—	—	55.5		
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_C = 60mA, T_j = 25^\circ C$	6.5	7.0	7.5	V		
$I_{GES}$	Gate leakage current	$V_{GE} = V_{GES}, V_{CE} = 0V, T_j = 25^\circ C$	-0.5	—	0.5	$\mu A$		
$C_{ies}$	Input capacitance	$V_{CE} = 10V, V_{GE} = 0V, f = 100kHz$ $T_j = 25^\circ C$	—	53.4	—	nF		
$C_{oes}$	Output capacitance		—	3.8	—	nF		
$C_{res}$	Reverse transfer capacitance		—	0.5	—	nF		
$Q_G$	Total gate charge	$V_{CC} = 1800V, I_C = 600A, V_{GE} = \pm 15V$	—	3.6	—	$\mu C$		
$V_{CEsat}$	Collector-emitter saturation voltage	$I_C = 600A$ (Note 5) $V_{GE} = 15V$	$T_j = 25^\circ C$	—	2.30	—	V	
			$T_j = 125^\circ C$	—	2.80	—		
			$T_j = 150^\circ C$	—	2.90	3.30		
$t_{d(on)}$	Turn-on delay time	$V_{CC} = 1800V$ $I_C = 600A$ $V_{GE} = \pm 15V$ $R_{G(on)} = 2.2\Omega$ $L_s = 65nH$ Inductive load $C_{GE} = 33nF$	$T_j = 150^\circ C$	—	—	1.25	J	
$t_r$	Rise time		$T_j = 150^\circ C$	—	—	0.50		
$E_{on(10\%)}$	Turn-on switching energy (Note 5)		$T_j = 25^\circ C$	—	0.76	—		J
			$T_j = 125^\circ C$	—	0.92	—		
			$T_j = 150^\circ C$	—	0.93	—		
$E_{on}$	Turn-on switching energy	$T_j = 25^\circ C$	—	0.82	—	J		
		$T_j = 125^\circ C$	—	0.99	—			
		$T_j = 150^\circ C$	—	1.00	—			
$t_{d(off)}$	Turn-off delay time	$V_{CC} = 1800V$ $I_C = 600A$ $V_{GE} = \pm 15V$ $R_{G(off)} = 51\Omega$ $L_s = 65nH$ Inductive load $C_{GE} = 33nF$	$T_j = 25^\circ C$	—	3.40	—	$\mu s$	
			$T_j = 125^\circ C$	—	3.60	—		
			$T_j = 150^\circ C$	—	3.65	5.00		
$t_f$	Fall time		$T_j = 25^\circ C$	—	0.23	—	$\mu s$	
			$T_j = 125^\circ C$	—	0.33	—		
		$T_j = 150^\circ C$	—	0.35	1.00			
$E_{off(10\%)}$	Turn-off switching energy per pulse (Note 5)	$T_j = 25^\circ C$	—	0.67	—	J		
		$T_j = 125^\circ C$	—	0.91	—			
		$T_j = 150^\circ C$	—	0.92	—			
$E_{off}$	Turn-off switching energy per pulse	$T_j = 25^\circ C$	—	0.76	—	J		
		$T_j = 125^\circ C$	—	1.03	—			
		$T_j = 150^\circ C$	—	1.04	—			
$V_{EC}$	Emitter-collector voltage (Note 2)	$I_E = 600A$ (Note 5) $V_{GE} = 0V$	$T_j = 25^\circ C$	—	2.10	—	V	
			$T_j = 125^\circ C$	—	2.30	—		
			$T_j = 150^\circ C$	—	2.40	2.90		

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## ELECTRICAL CHARACTERISTICS (Clamp-Di part)

Symbol	Item	Conditions	Limits			Unit	
			Min	Typ	Max		
I <sub>RRM</sub>	Repetitive reverse current (Note 3)	V <sub>AK</sub> = V <sub>RRM</sub>	T <sub>J</sub> = 25°C	—	—	1.0	mA
			T <sub>J</sub> = 125°C	—	1.0	—	
			T <sub>J</sub> = 150°C	—	—	18.0	
V <sub>FM</sub>	Forward voltage (Note 3)	I <sub>F</sub> = 600 A (Note 5)	T <sub>J</sub> = 25°C	—	2.10	—	V
			T <sub>J</sub> = 125°C	—	2.30	—	
			T <sub>J</sub> = 150°C	—	2.40	2.90	
t <sub>rr</sub>	Reverse recovery time (Note 3)		T <sub>J</sub> = 25°C	—	0.55	—	μs
			T <sub>J</sub> = 125°C	—	0.65	—	
			T <sub>J</sub> = 150°C	—	0.70	—	
I <sub>rr</sub>	Reverse recovery current (Note 3)		T <sub>J</sub> = 25°C	—	1170	—	A
			T <sub>J</sub> = 125°C	—	1120	—	
			T <sub>J</sub> = 150°C	—	1100	—	
Q <sub>rr(10%)</sub>	Reverse recovery charge (Note 3), (Note 7)	V <sub>CC</sub> = 1800 V I <sub>C</sub> = 600 A V <sub>GE</sub> = ±15 V R <sub>G(on)</sub> = 2.2 Ω L <sub>s</sub> = 65nH Inductive load C <sub>GE</sub> = 33 nF	T <sub>J</sub> = 25°C	—	620	—	μC
			T <sub>J</sub> = 125°C	—	740	—	
			T <sub>J</sub> = 150°C	—	770	—	
Q <sub>rr</sub>	Reverse recovery charge (Note 3)		T <sub>J</sub> = 25°C	—	650	—	μC
			T <sub>J</sub> = 125°C	—	805	—	
			T <sub>J</sub> = 150°C	—	845	—	
E <sub>rec(10%)</sub>	Reverse recovery energy per pulse (Note 3), (Note 6)		T <sub>J</sub> = 25°C	—	0.66	—	J
			T <sub>J</sub> = 125°C	—	0.88	—	
			T <sub>J</sub> = 150°C	—	0.91	—	
E <sub>rec</sub>	Reverse recovery energy per pulse (Note 3)		T <sub>J</sub> = 25°C	—	0.75	—	J
			T <sub>J</sub> = 125°C	—	1.01	—	
			T <sub>J</sub> = 150°C	—	1.03	—	

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## THERMAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$R_{th(f-c)Q}$	Thermal resistance	Junction to Case, IGBT part , 1/2 module	—	—	20.5	K/kW
$R_{th(f-c)D}$	Thermal resistance	Junction to Case, FWDi part, per 1/2 module	—	—	34.0	K/kW
$R_{th(f-c)D}$	Thermal resistance	Junction to Case, Clamp-Di part	—	—	34.0	K/kW
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, Switching part , 1/2 module $\lambda_{grease} = 1W/m \cdot k, D_{(c-s)} = 70\mu m$	—	16.0	—	K/kW

## MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
$M_t$	Mounting torque	Main terminals screw M8	7.0	—	14.0	N·m
$M_s$		Mounting screw M6	3.0	—	6.0	N·m
$M_t$		Auxiliary terminals screw M3	0.4	—	0.8	N·m
$m$	Mass		—	0.75	—	kg
CTI	Comparative tracking index		600	—	—	—
$d_a$	Clearance	Between terminals and baseplate	19.5	—	—	mm
$d_s$	Creepage distance	Between terminals and baseplate	32.0	—	—	mm
$L_{P-P-N}$	Parasitic stray inductance	Between terminal 1, 2 and terminal 6, 7	—	10.0	—	nH
$R_{CC+EE}$	Internal lead resistance	$T_C = 25\text{ }^\circ\text{C}$ , Between terminal 1, 2 and terminal 3, 4, 5	—	0.28	—	mΩ
$R_{AA+KK}$		$T_C = 25\text{ }^\circ\text{C}$ , Between terminal 3, 4, 5 and terminal 6, 7	—	0.18	—	mΩ
$r_g$	Internal gate resistance	$T_C = 25\text{ }^\circ\text{C}$	—	0.83	—	Ω

Note1. Pulse width and repetition rate should be such that junction temperature ( $T_j$ ) does not exceed  $T_{jopmax}$  rating.

- The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD).
- The symbols represent characteristics of the clamp diode (Clamp-Di).
- Junction temperature ( $T_j$ ) should not exceed  $T_{jmax}$  rating (150°C).
- Pulse width and repetition rate should be such as to cause negligible temperature rise.
- The integration range of switching energies is from 10% $V_{CE}$  to 10% $I_C$ (10% $I_E$ ).
- The integration range of reverse recovery charge is from  $I_E = 0A$  to 10% $I_E$ .

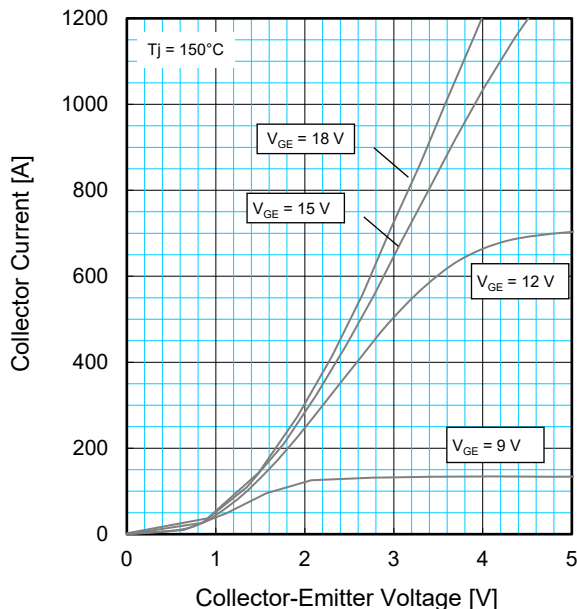
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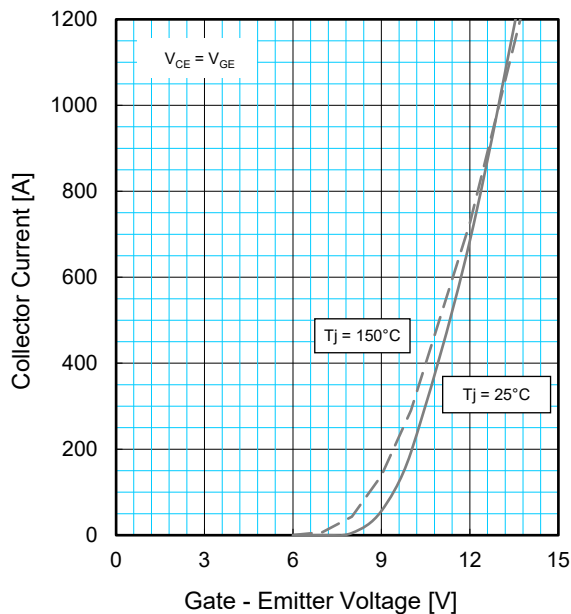
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## PERFORMANCE CURVES

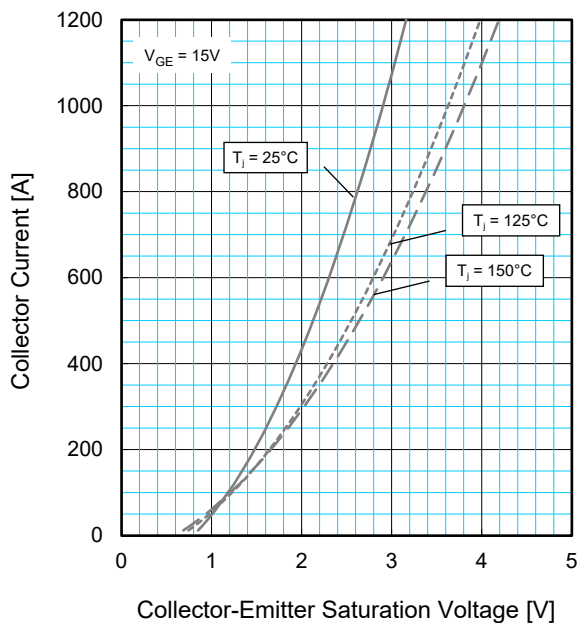
**OUTPUT CHARACTERISTICS (TYPICAL)**



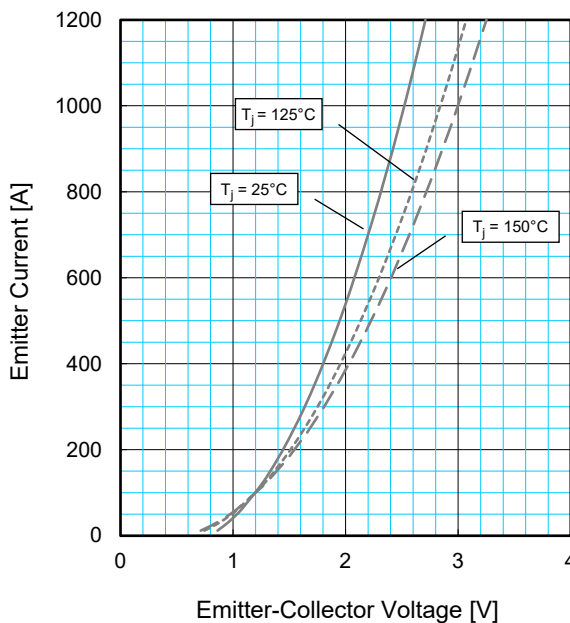
**TRANSFER CHARACTERISTICS (TYPICAL)**



**COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



**DIODE FORWARD CHARACTERISTICS (TYPICAL)**



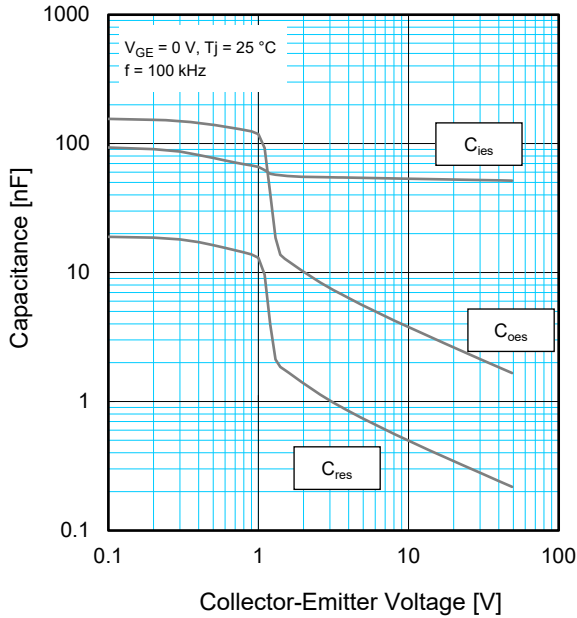
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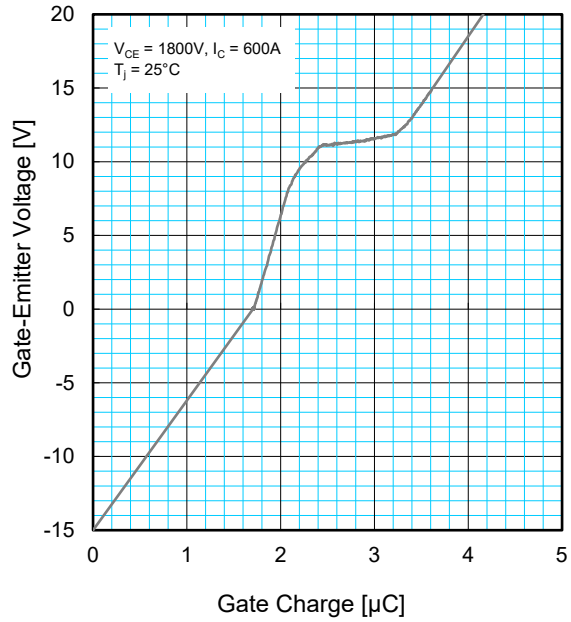
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## PERFORMANCE CURVES

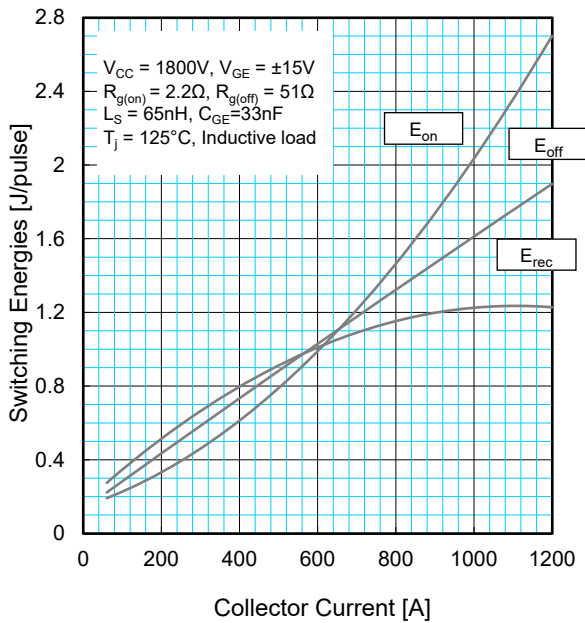
**CAPACITANCE CHARACTERISTICS (TYPICAL)**



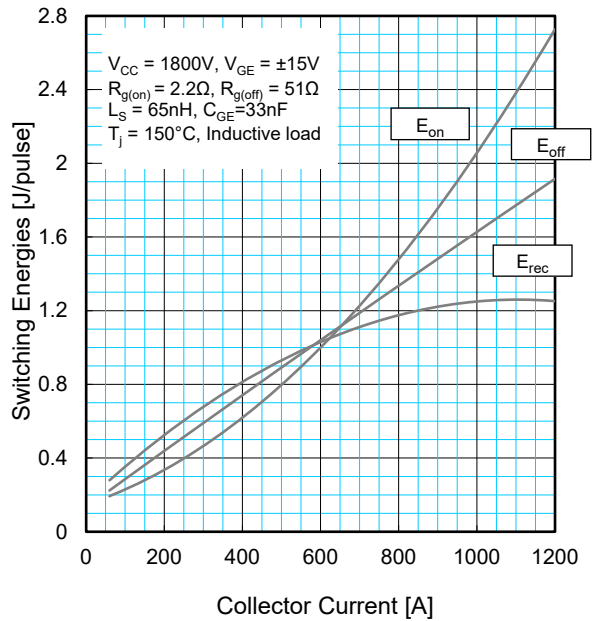
**GATE CHARGE CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



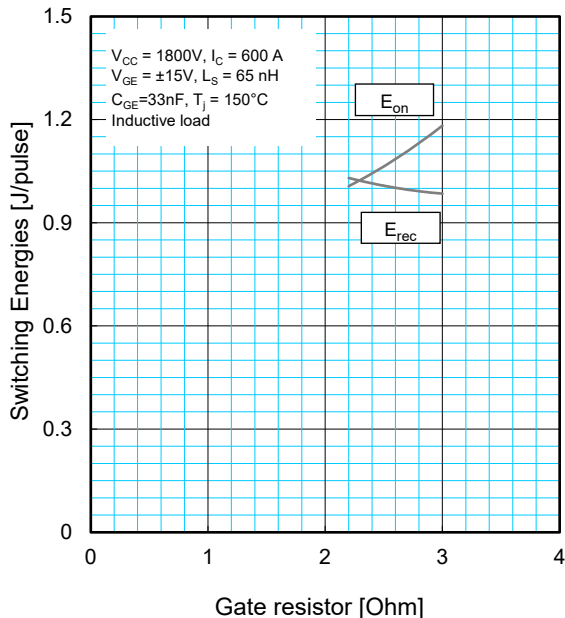
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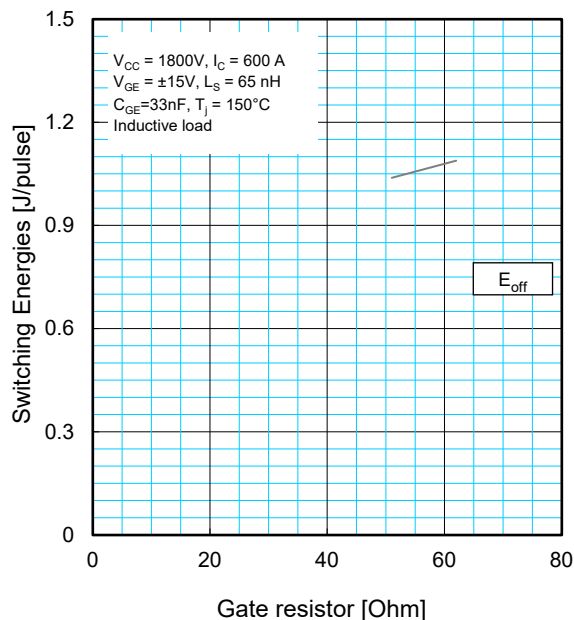
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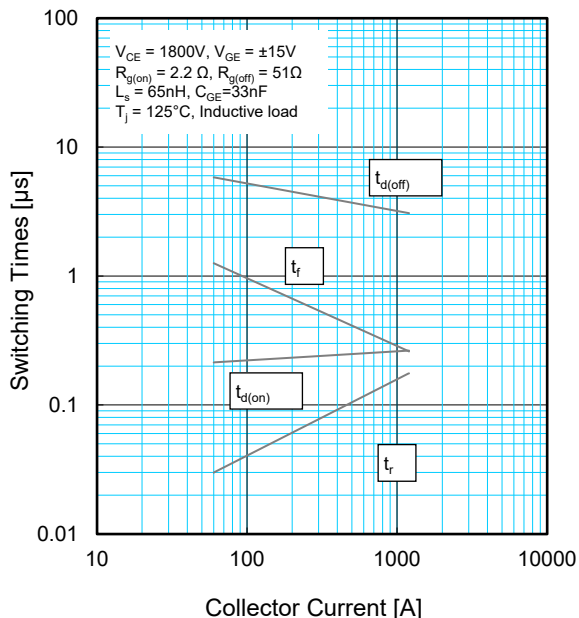
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



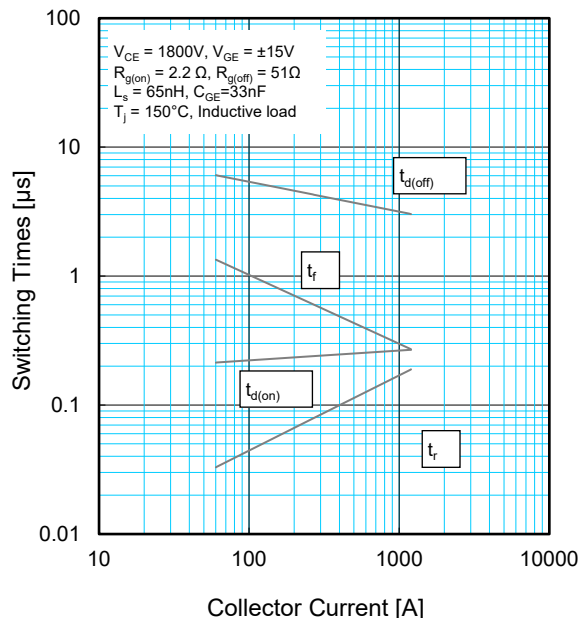
**HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**



**HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)**





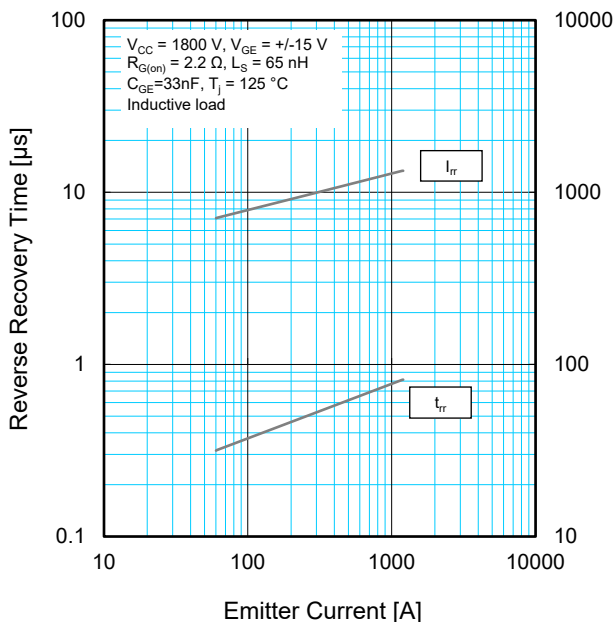
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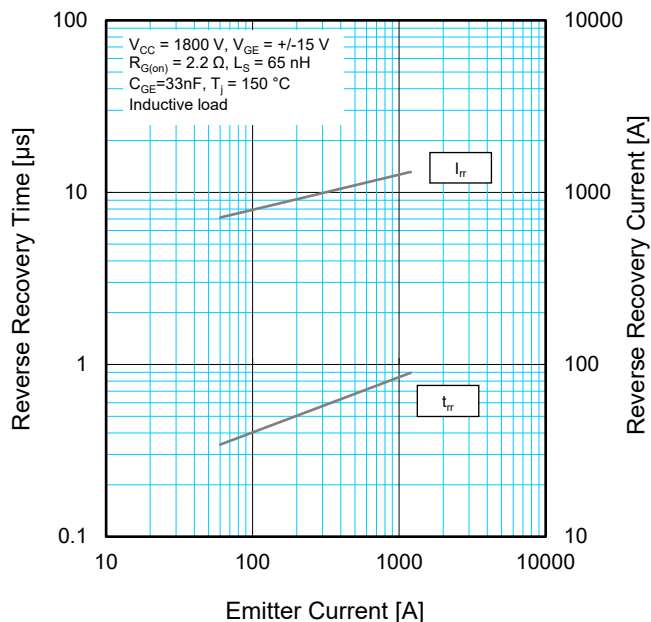
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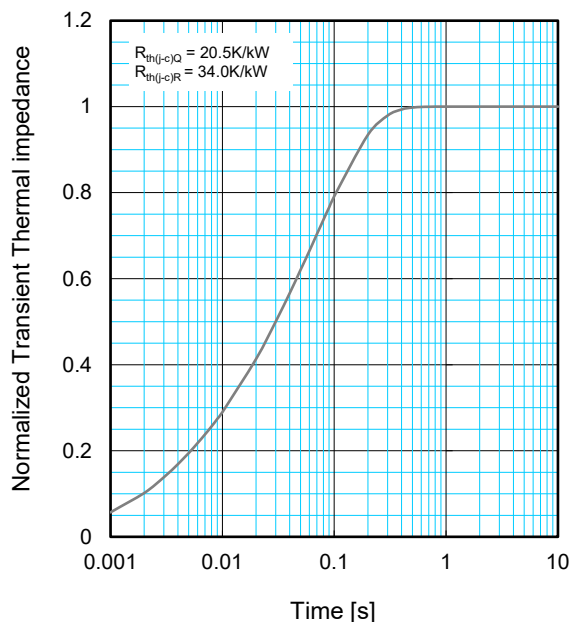
**DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



**DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS**



$$Z_{th(j-c)}(t) = \sum_{i=1}^n R_i \left\{ 1 - \exp\left(-\frac{t}{\tau_i}\right) \right\}$$

	1	2	3	4
$R_i / R_{th}$ :	0.0292	0.0832	0.2277	0.6599
$\tau_i$ [sec.] :	0.0025	0.0027	0.0155	0.0865

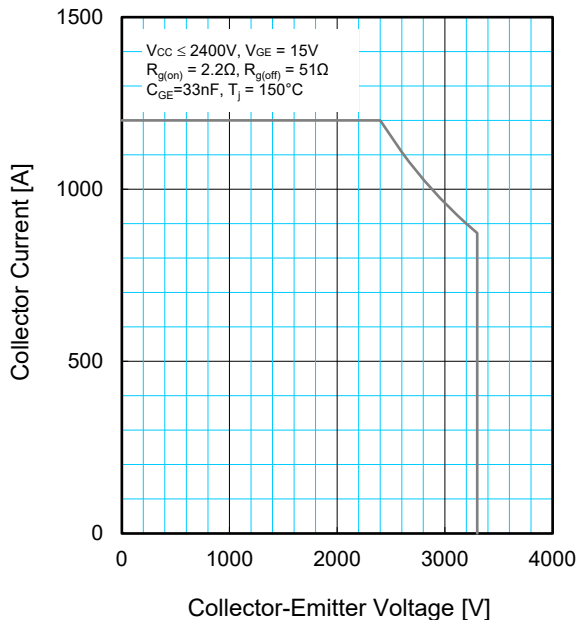
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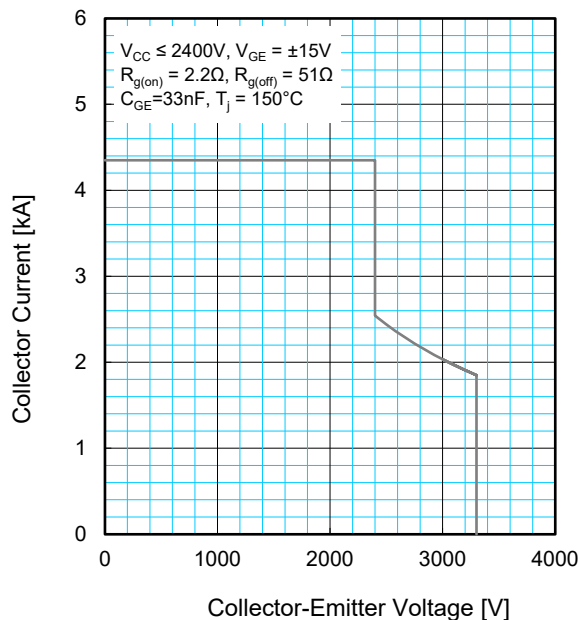
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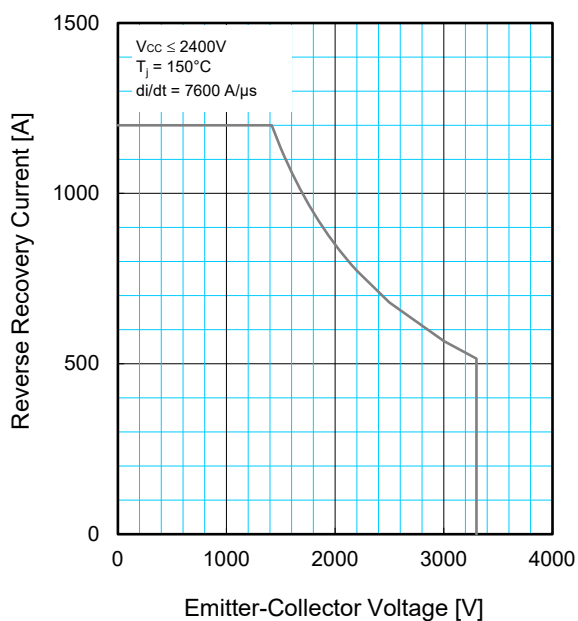
**REVERSE BIAS SAFE OPERATING AREA (RBSOA)**



**SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)**



**DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)**



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